

FIG. 12

CONVENTIONAL ART

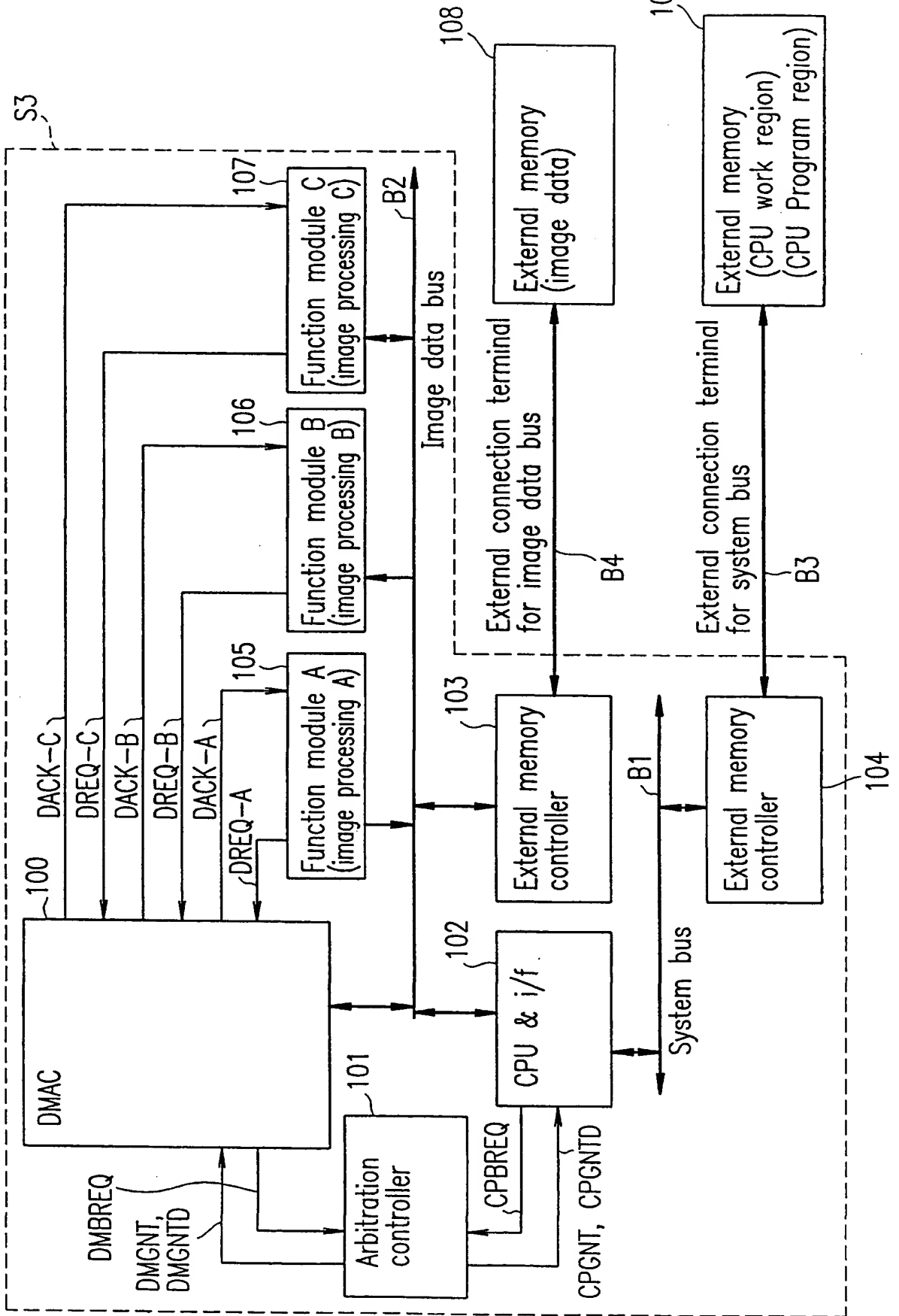
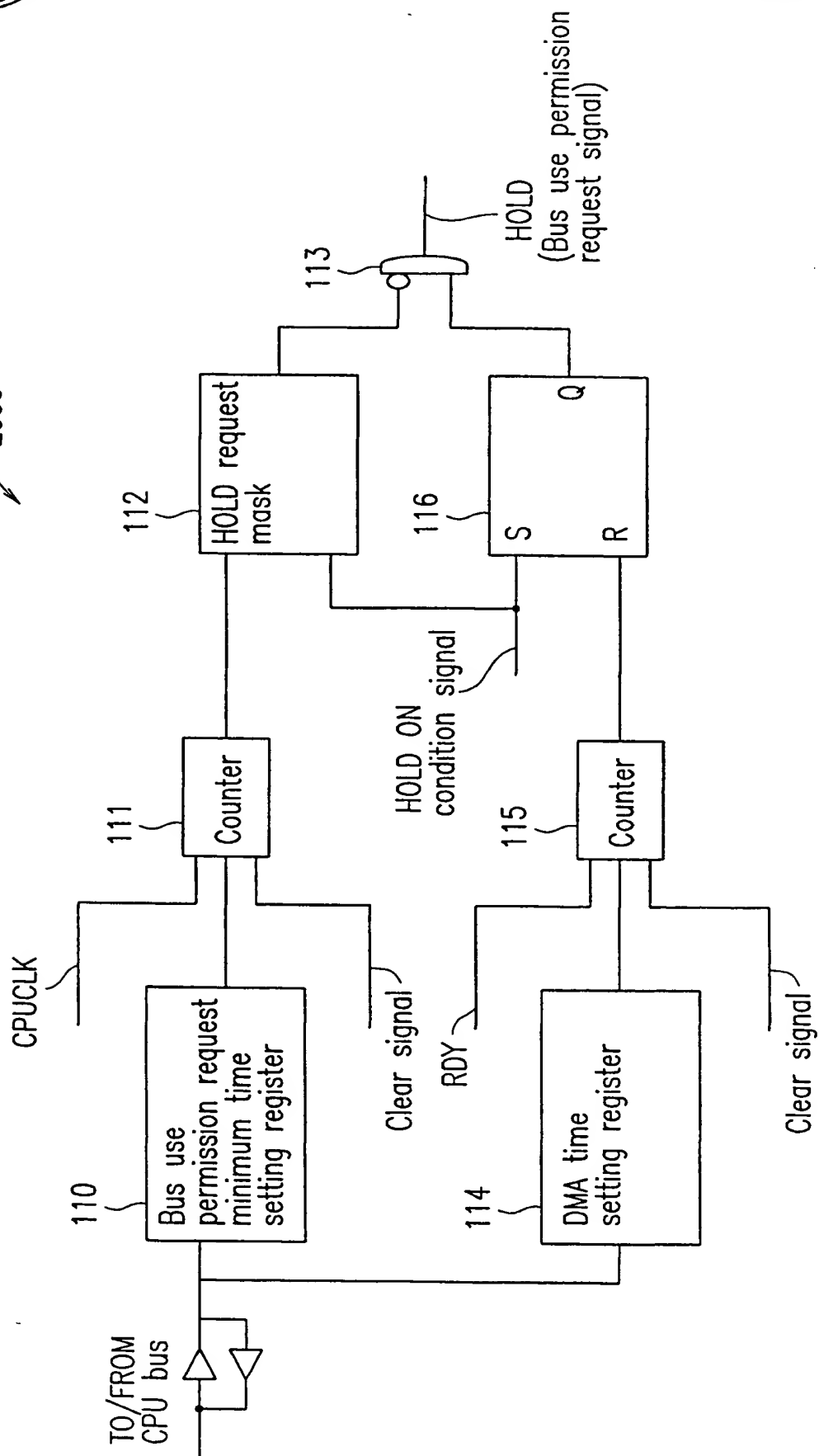


FIG. 13

CONVENTIONAL ART

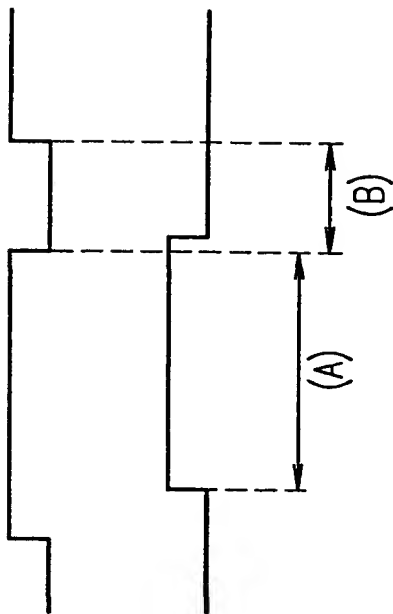
2000



**FIG. 14**  
CONVENTIONAL ART

(1) HOLD  
(bus use permission request signal)

(2) HOLDACK  
(bus use permission signal)



(A) DMA transfer time  
(CPU or DMAC which has bus use permission uses bus for data transfer)

(B) Bus use permission request minimum time  
(time consumed until a bus use permission can be requested after the data transfer is completed during time (A) is completed)